

Figure 1: Demodulator architecture with Time-domain windowing.

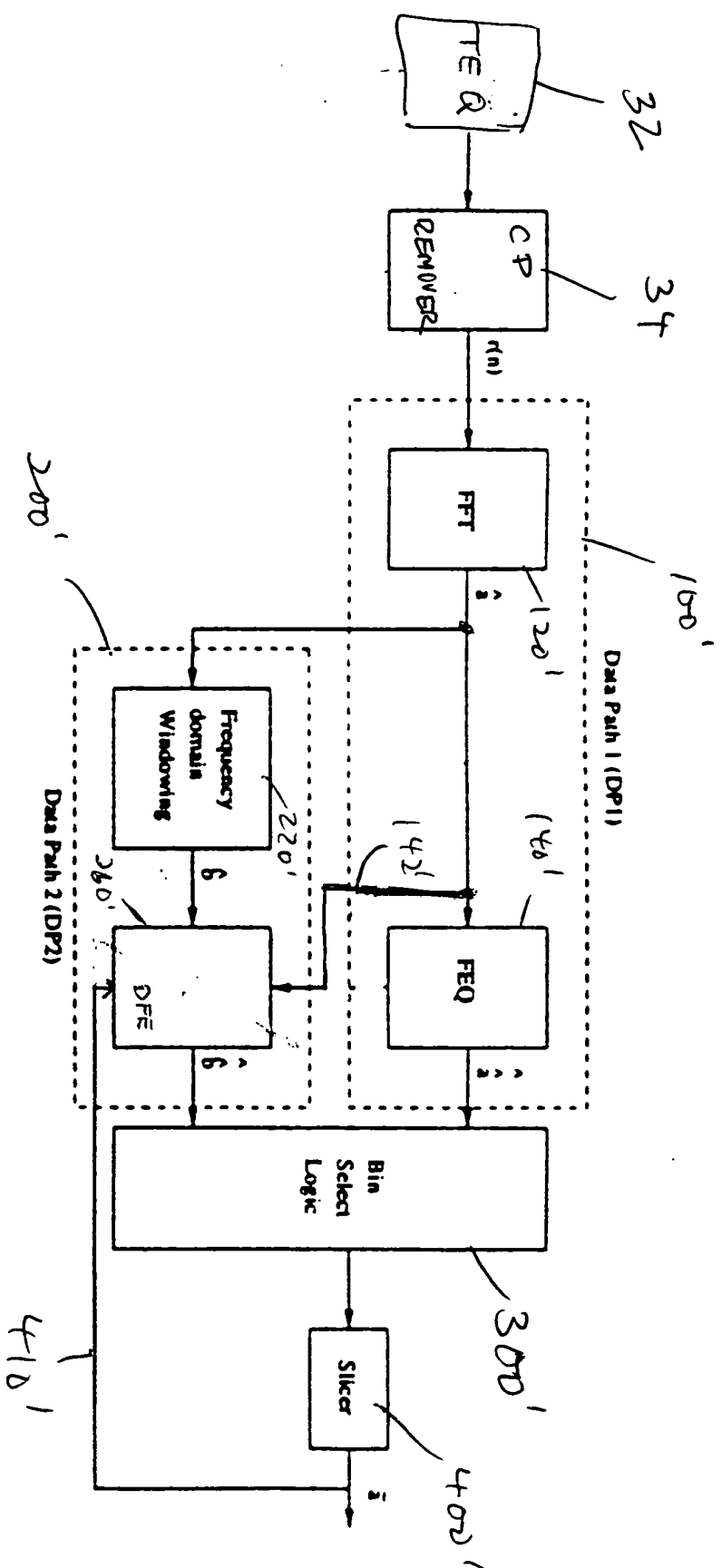


Figure 2: Demodulator architecture with Frequency-domain windowing.

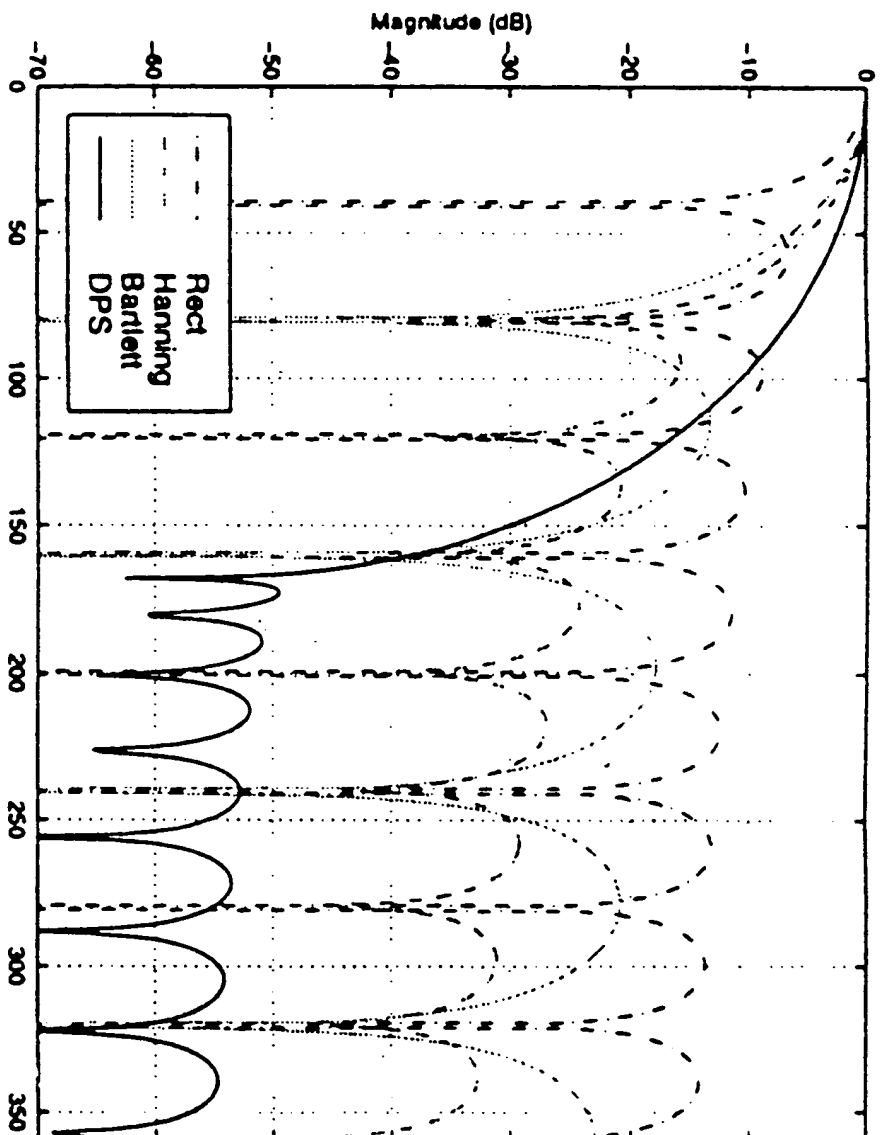


Figure 3: Magnitude response of various classical windows.

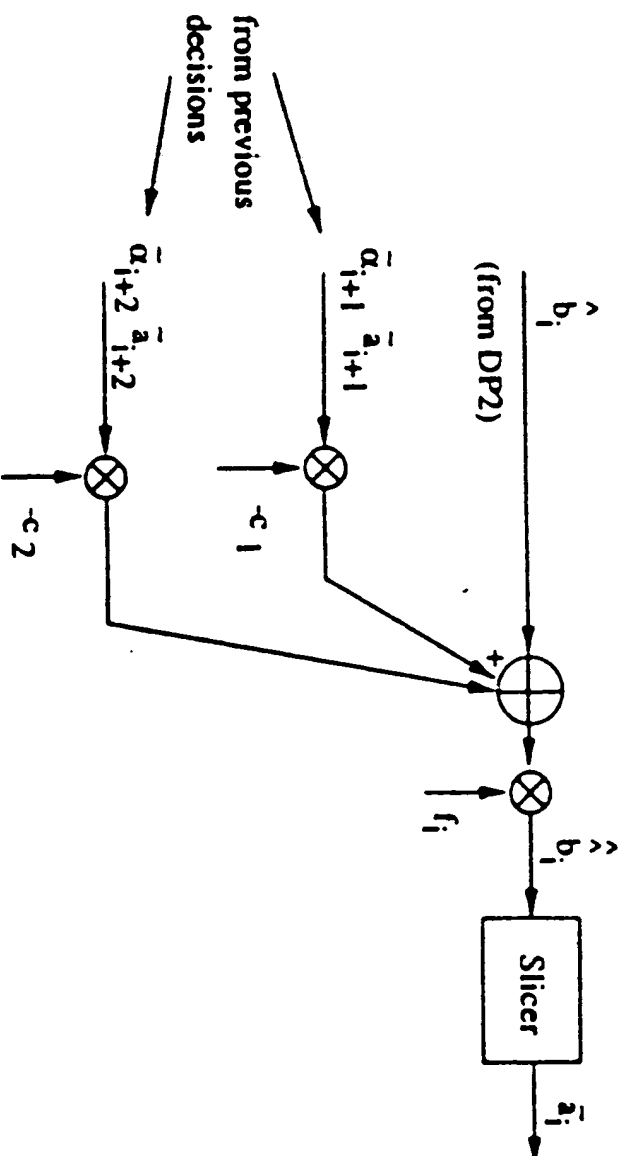


Figure 4: DFE with 2 feedback taps.

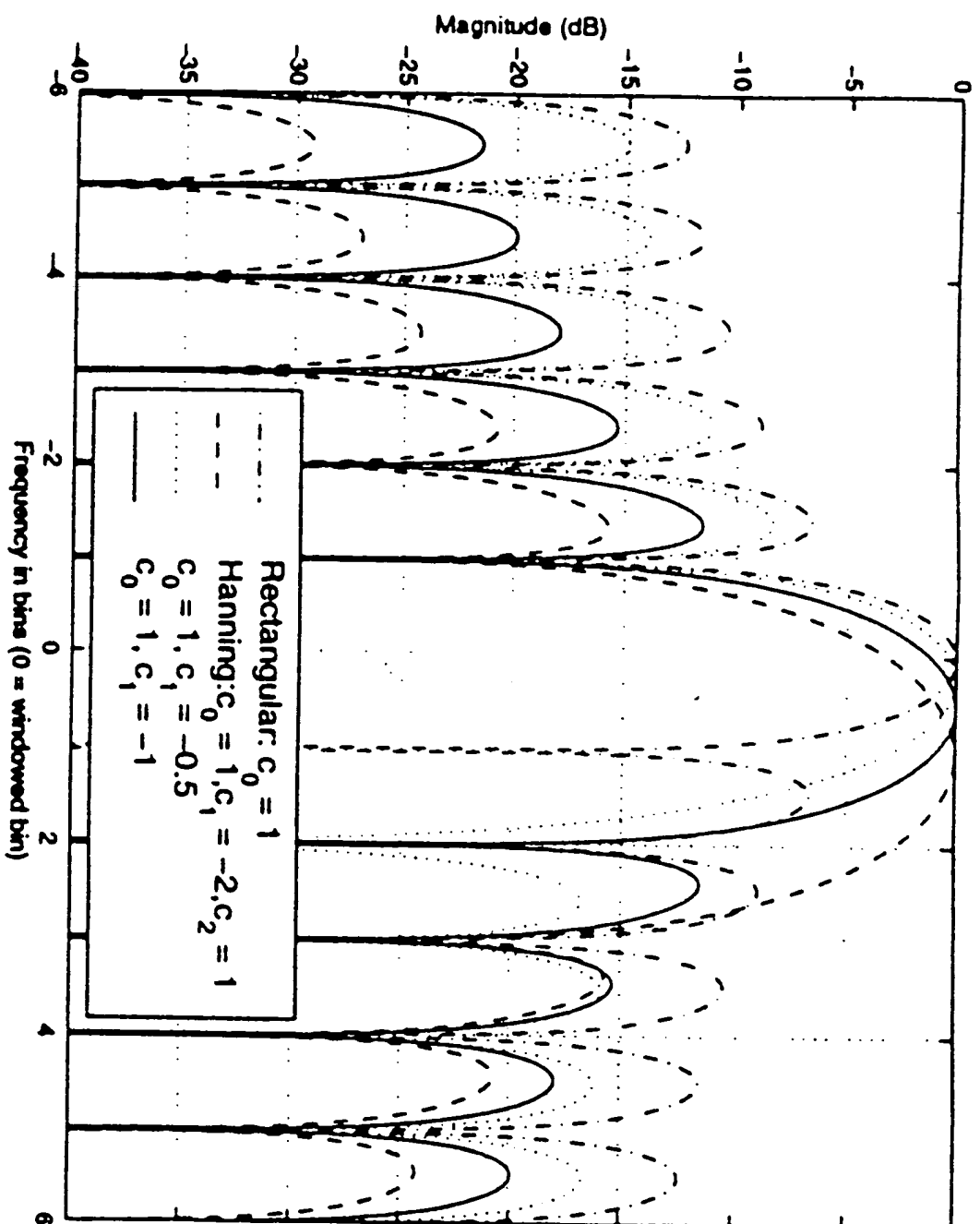
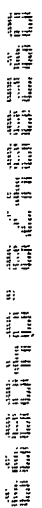
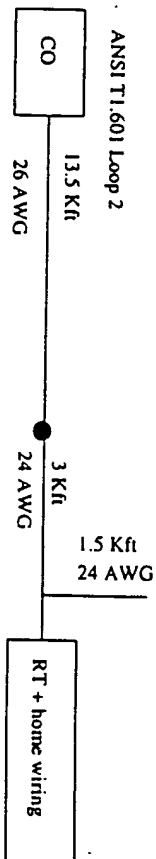


Figure 5: Different FDW magnitude responses.

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[illegible]

Long loop



Medium Loop

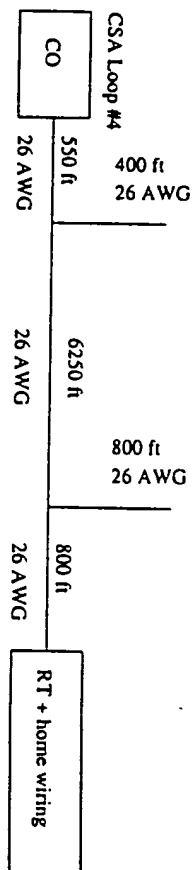


Figure 6: Long and Medium length loops.

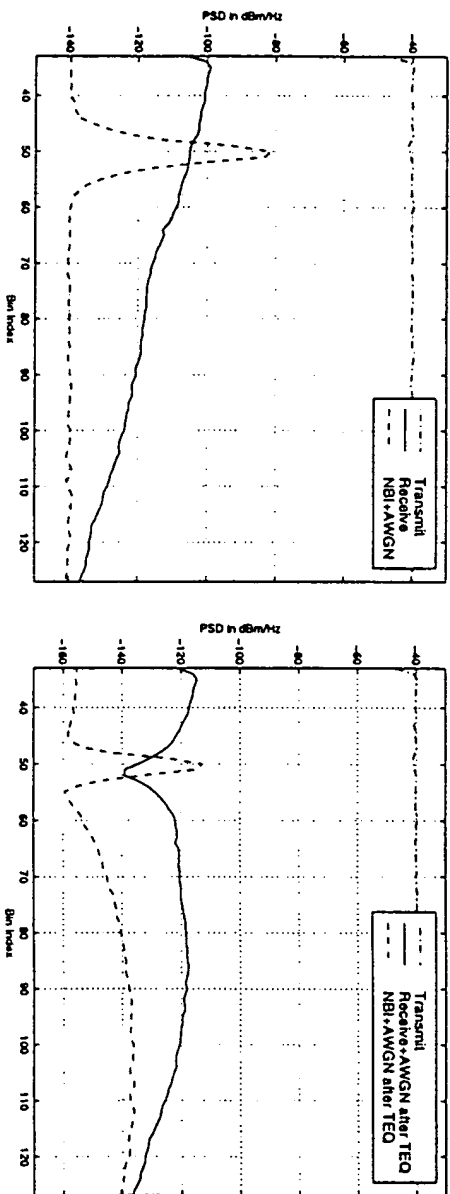


Figure 7: Long loop with NBI: (A) Signal and noise PSD's before TEO (B) After TEO.

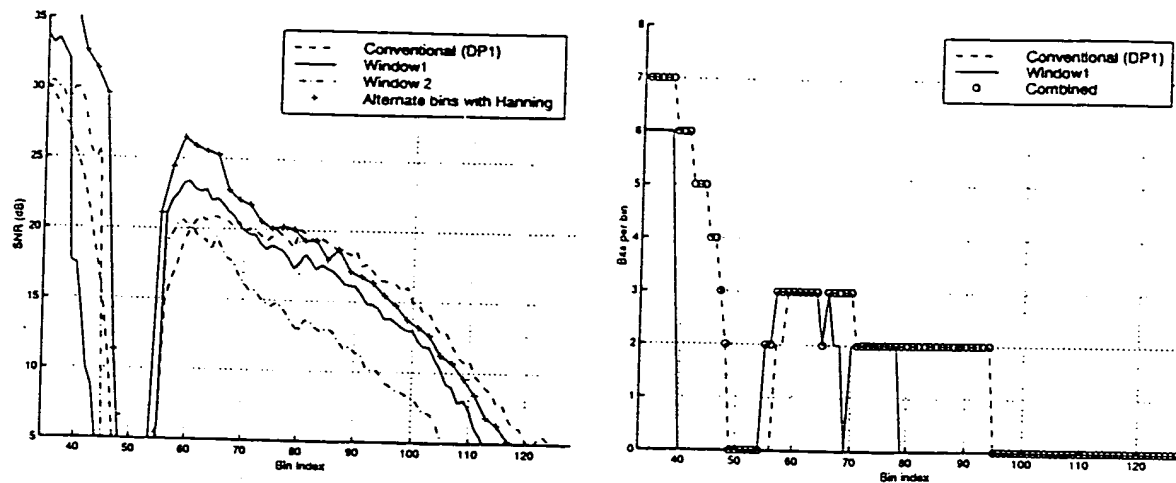


Figure 8: Long loop with NBI: (A) SNR profile (B) Bit-allocation profile.

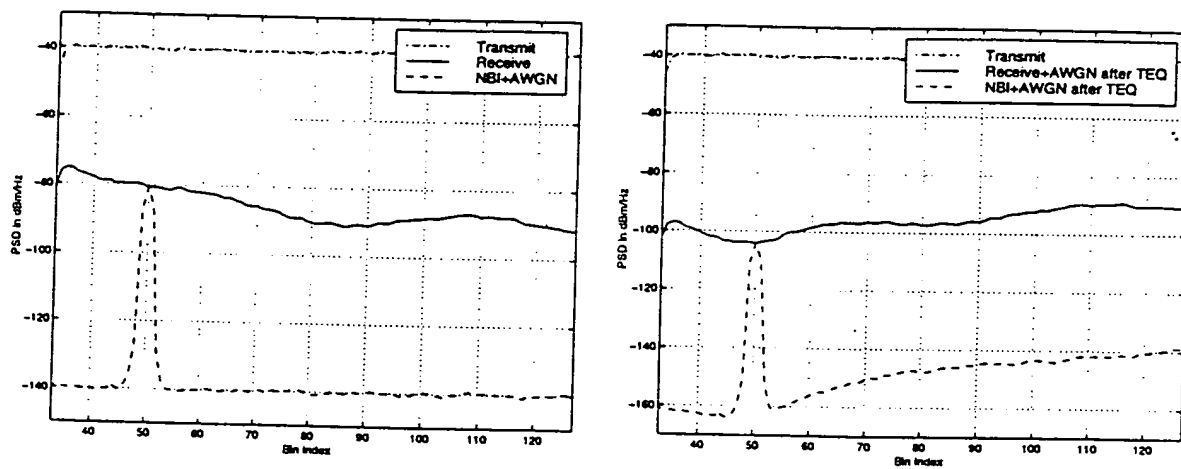


Figure 9: Medium loop with NBI: (A) Signal and noise PSD's before TEQ (B) After TEQ.

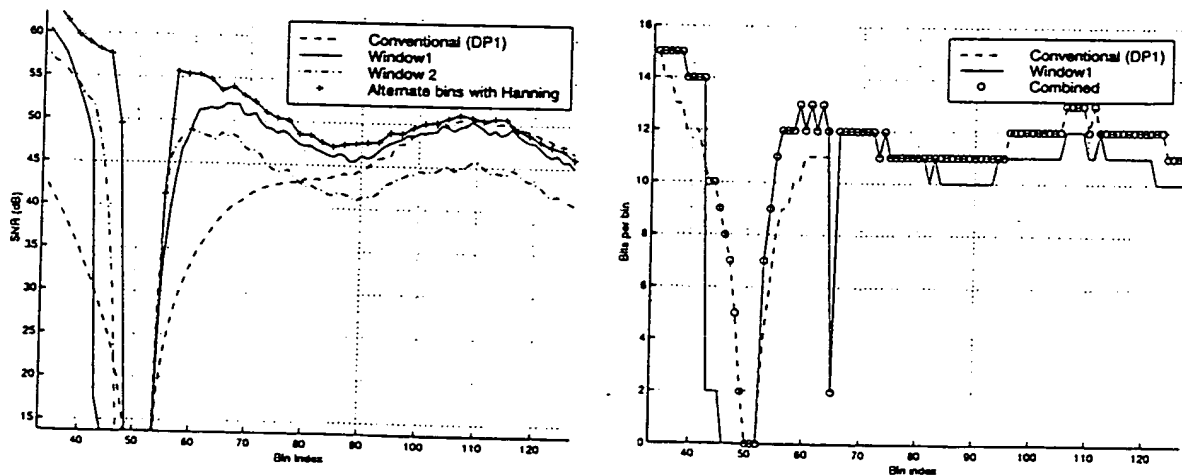


Figure 10: Medium loop with NBI: (A) SNR profile (B) Bit-allocation profile.

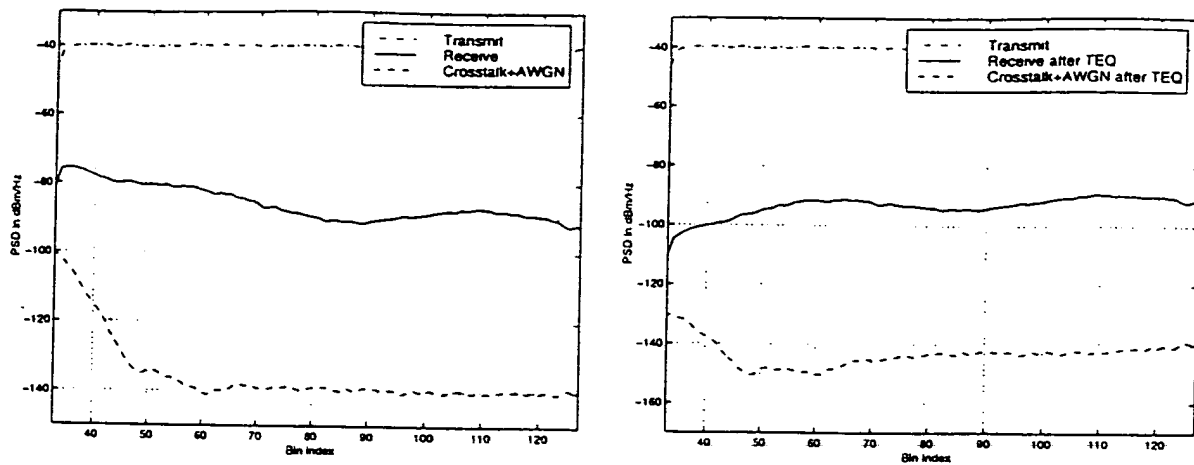


Figure 11: Medium loop with Crosstalk: (A) Signal and noise PSD's before TEQ (B) After TEQ.

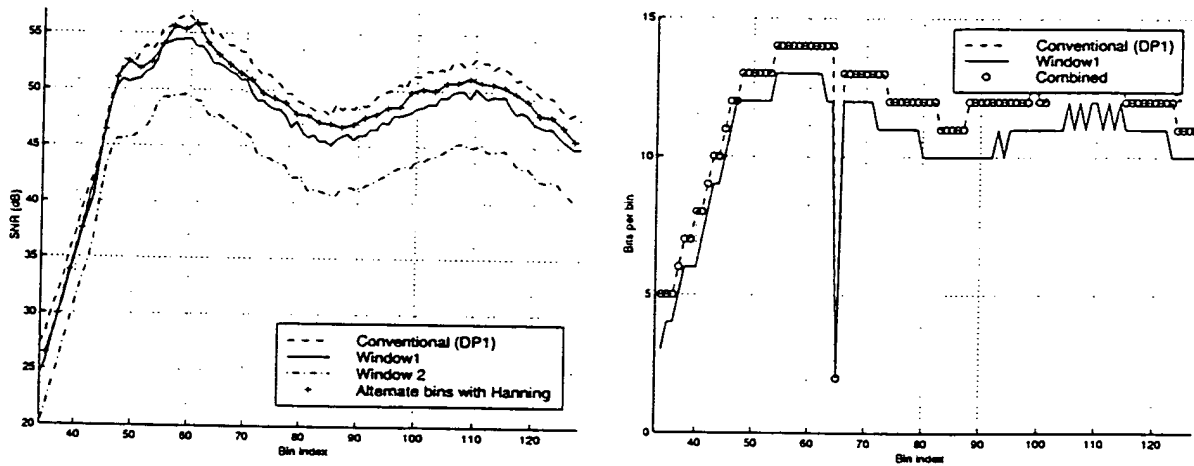


Figure 12: Medium loop with Crosstalk: (A) SNR profile (B) Bit-allocation profile.

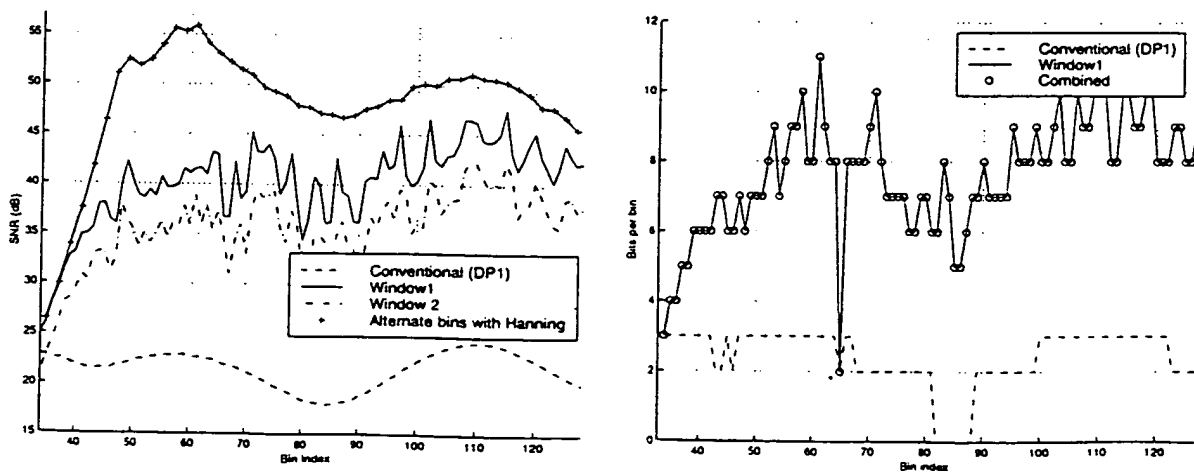


Figure 13: Medium loop with Crosstalk without TEQ: (A) SNR profile (B) Bit-allocation profile.

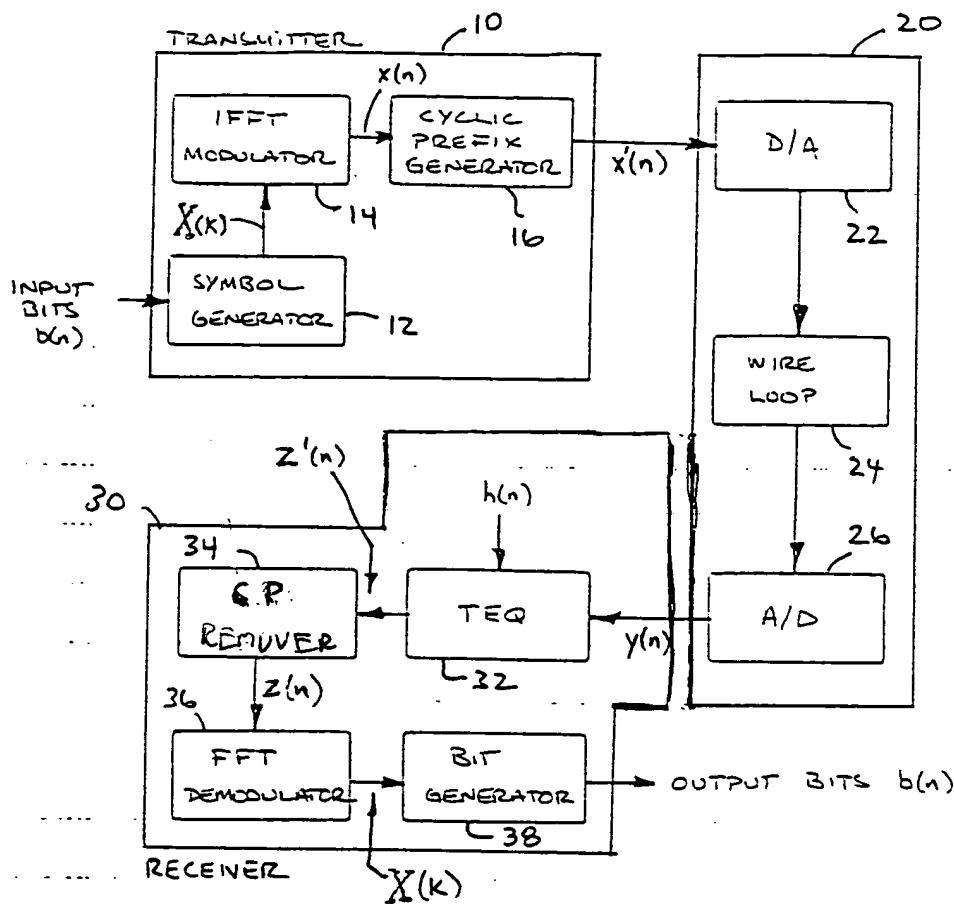


FIG 14
(Prior Art)

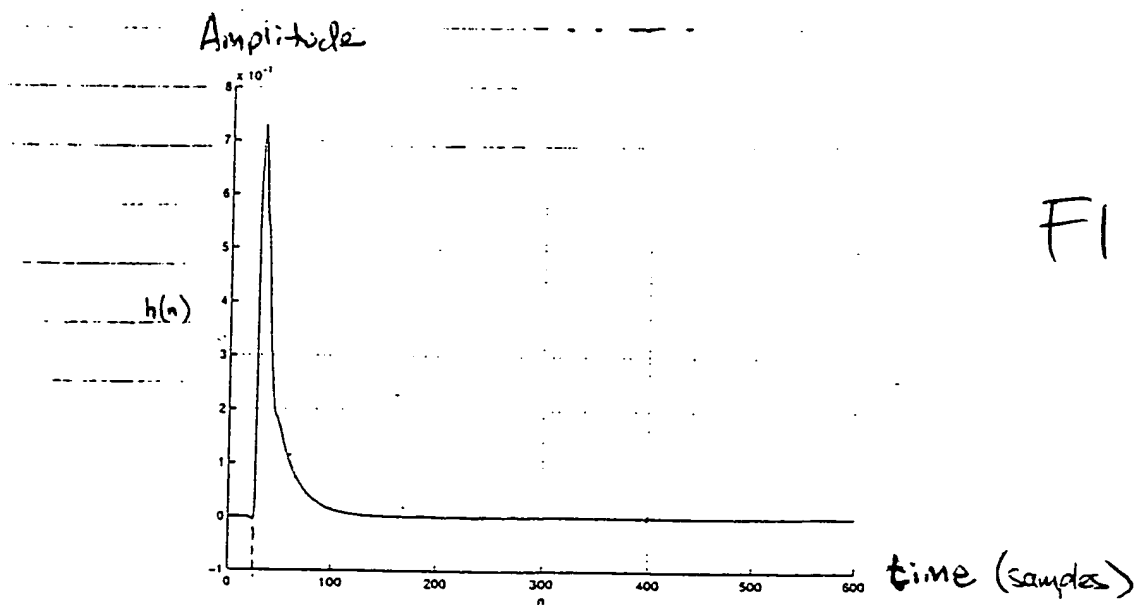


FIG 15